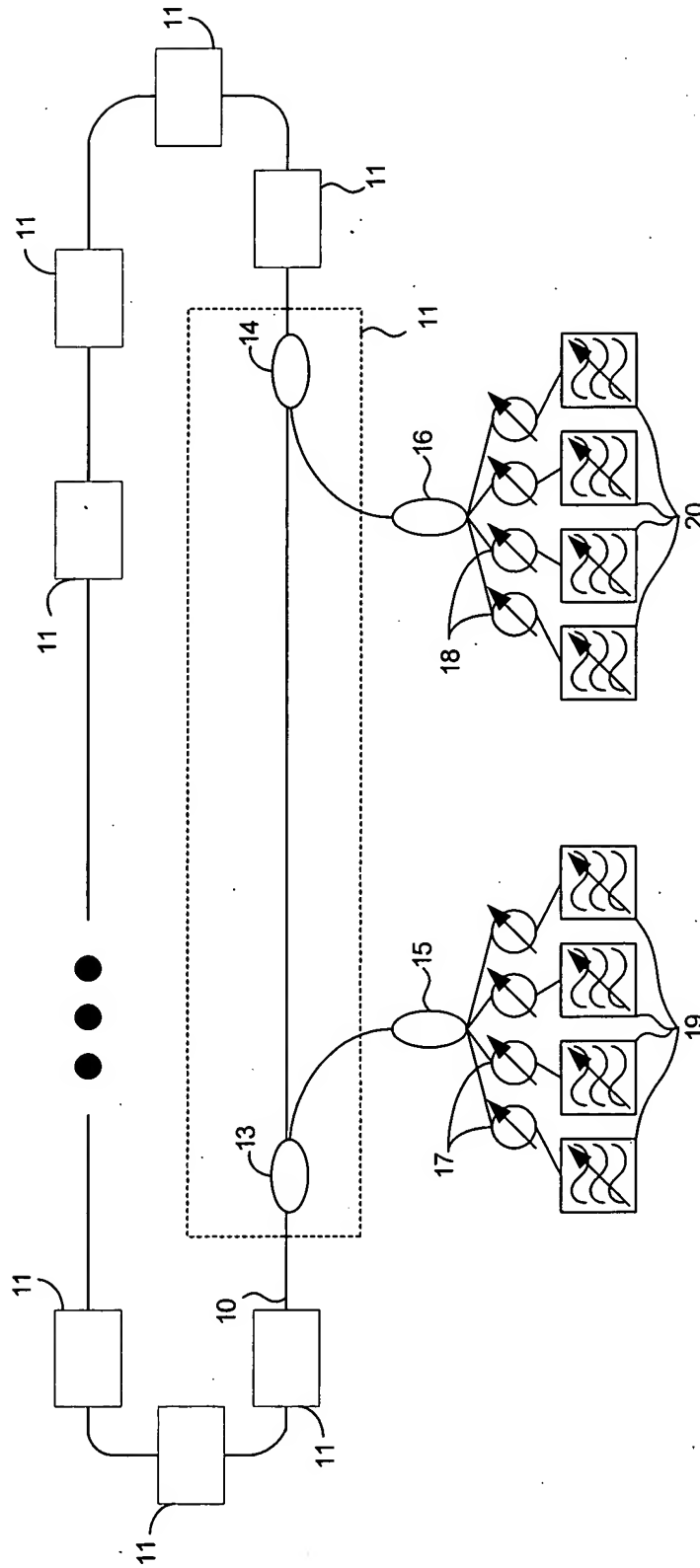




REPLACEMENT SHEET



Prior Art

FIG. 1A

Figure 1 is a schematic diagram of a system architecture. The system includes a central processing unit (10) connected to multiple input/output blocks (12). A dashed box (21) encloses a sub-system containing a central block (13) and a feedback loop (14). The feedback loop (14) is connected to a series of blocks (12) and a final output block (12). The system also includes a series of blocks (12) connected to a final output block (12). The system is labeled "Prior Art".

FIG. 1B

REPLACEMENT SHEET

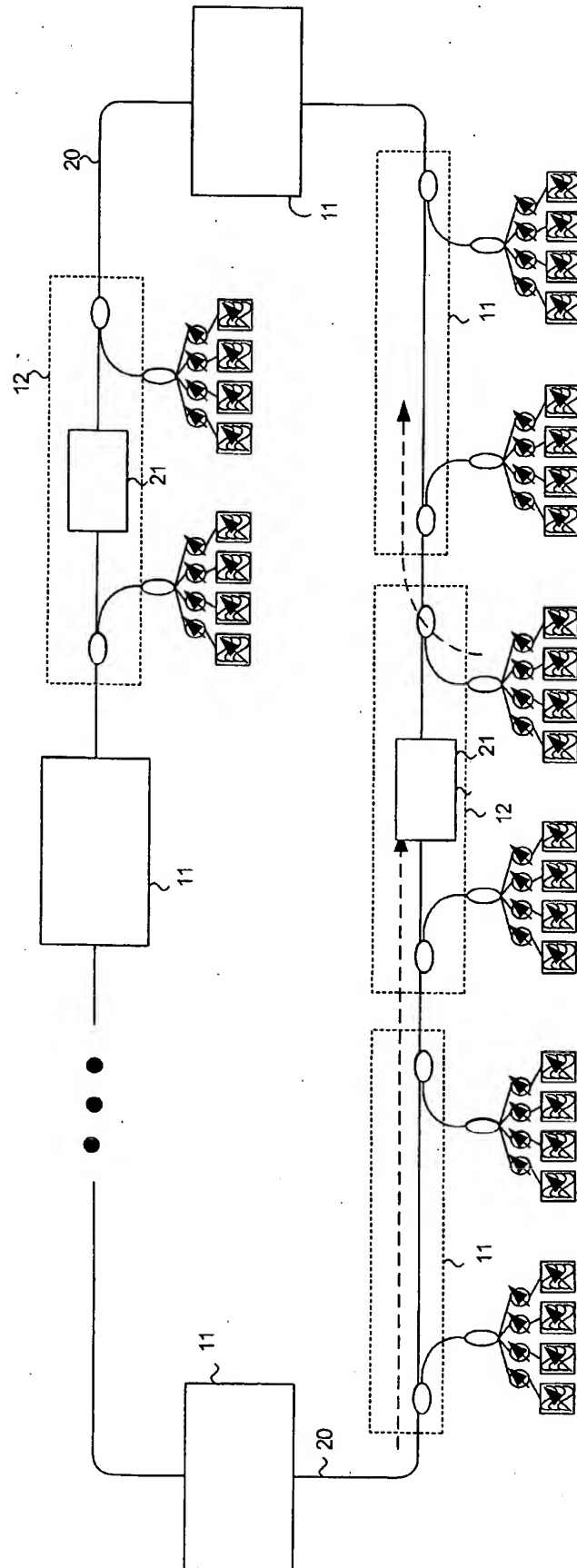


Fig. 2

REPLACEMENT SHEET

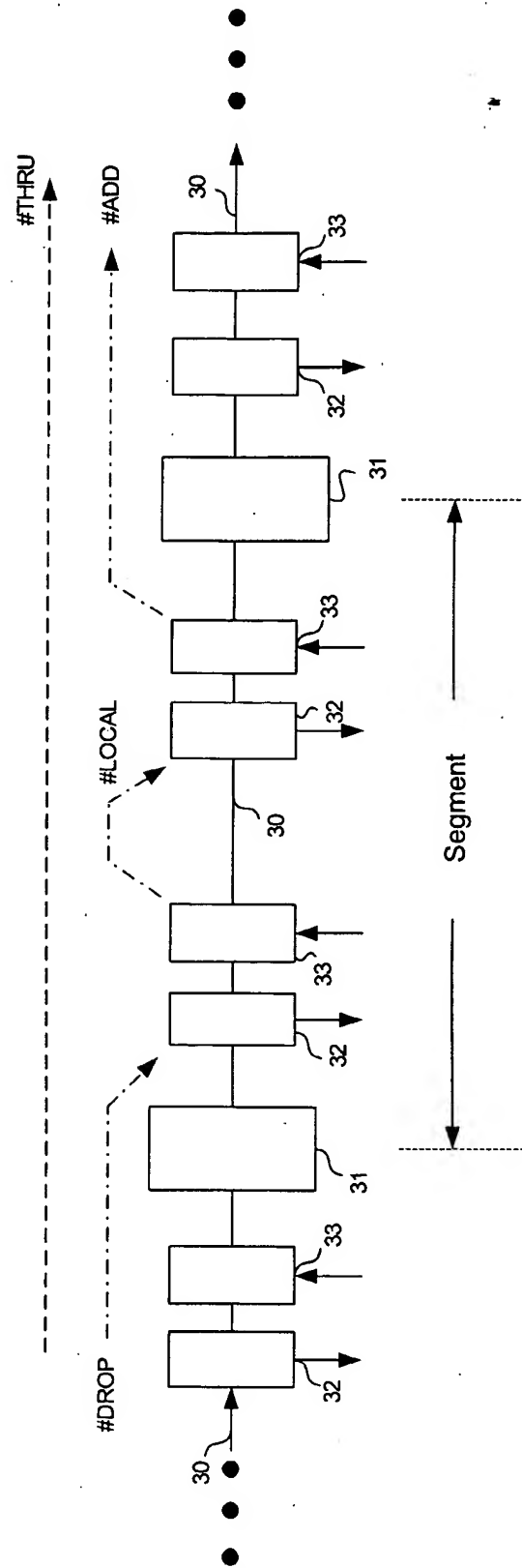


FIG. 3

REPLACEMENT SHEET

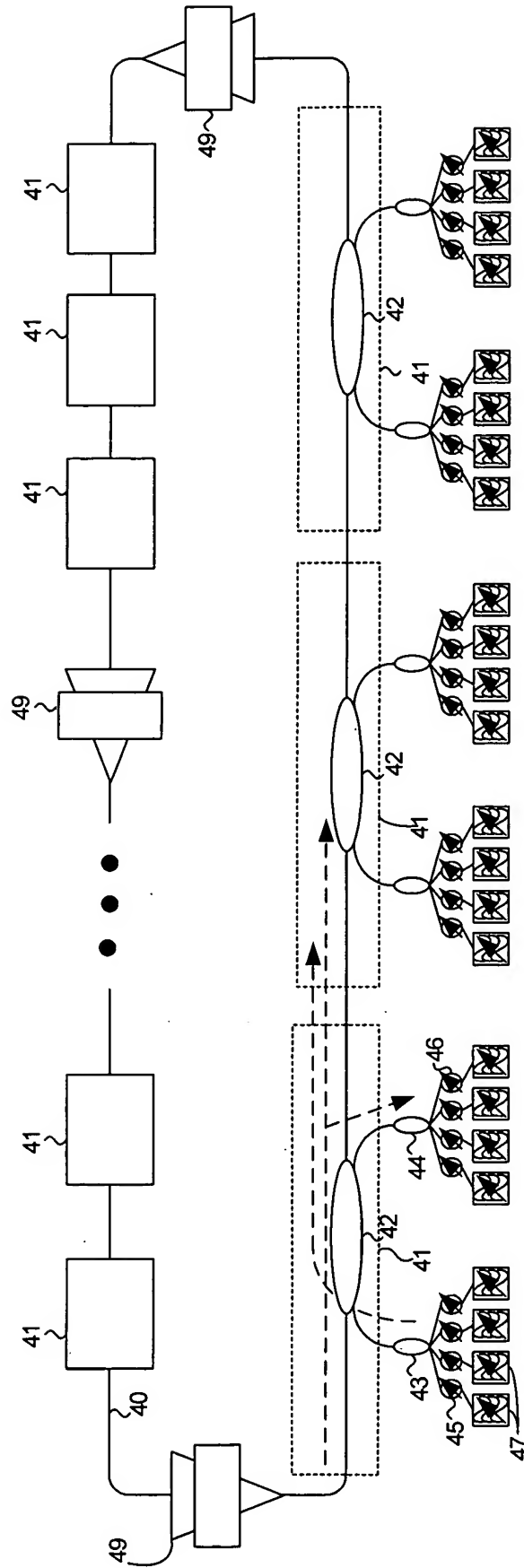


FIG. 4A

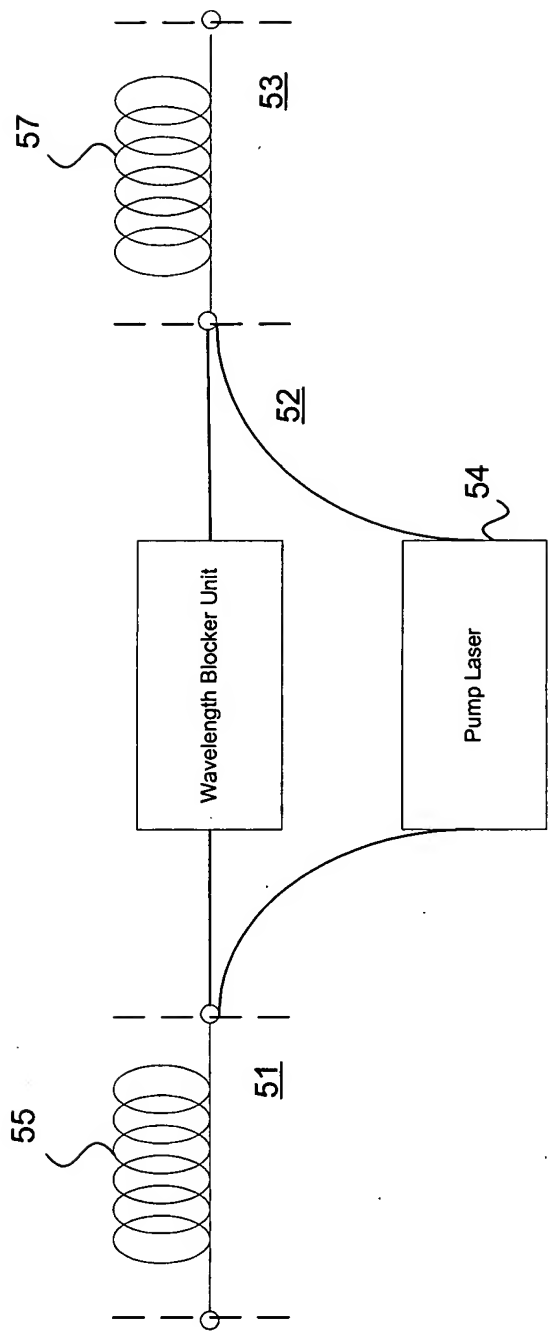


FIG. 4B

REPLACEMENT SHEET

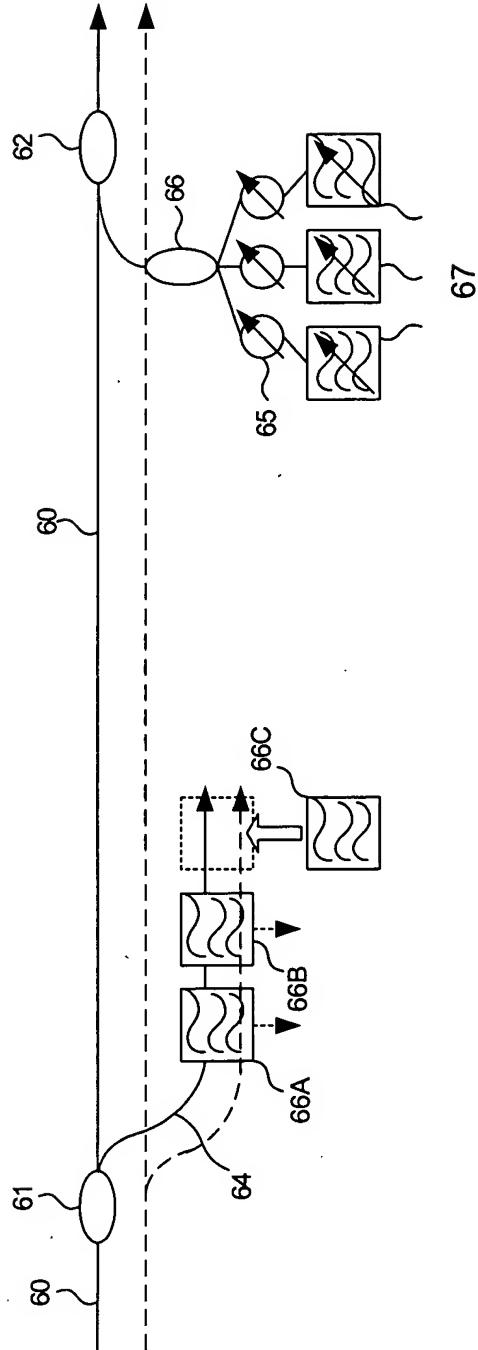


FIG. 5